



2811

PATENT
0630-1162P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: GIL, Gyoung-Seon Conf.:

Appl. No.: 09/689,746 Group: 2811

Filed: October 13, 2000 Examiner: LOKE, S.

For: THIN FILM TRANSISTOR AND FABRICATION
METHOD THEREFOR

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LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents
Washington, DC 20231

March 12, 2002

Sir:

Transmitted herewith is an amendment in the above-identified application.

- The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

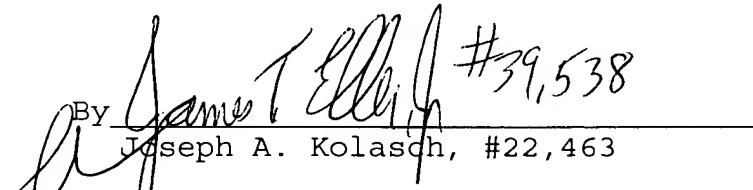
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	6	-	20	=	0	\$18	\$0.00
INDEPENDENT	1	-	3	=	0	\$80	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE-DEPENDENT CLAIM						-\$270	\$0.00
						TOTAL	\$0.00

- Petition for three (3) month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$920.00 for the extension of time.
- No fee is required.
- A check in the amount of \$920.00 is enclosed.
- Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP


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0630-1162P

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ATTACHMENT

(Rev. 01/22/01)



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: GIL, Gyoung-Seon

Conf:

Appl. No. 09/689,746

Group: 2811

Filed: October 13, 2000

Examiner: LOKE, S.

For: THIN FILM TRANSISTOR AND FABRICATION METHOD
THEREFOR

AMENDMENT

Assistant Commissioner
Washington, D.C. 20231

March 12, 2002

Dear Sir:

Responsive to the Examiner's Office Action dated October 3, 2001, the shortened statutory period for response being extended three (3) months to April 3, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION

Please replace the paragraph beginning on page 4, line 10, with the following rewritten paragraph:

B1

--FIG. 2 is a vertical cross-sectional view of a thin-film transistor

according to the present invention. As shown therein, a substrate 10 has a step formed on an upper surface thereof, and thus is provided with upper and